

Electronic Acknowledgement Receipt

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Application Number:	10583577
International Application Number:	
Confirmation Number:	1336
Title of Invention:	Monolithic Semiconductor Integrated Circuit And Method for Selective Memory Encryption And Decryption
First Named Inventor/Applicant Name:	Andrew Dellow
Customer Number:	38106
Filer:	Timothy Leroy Boller.
Filer Authorized By:	
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Application Type:	U.S. National Stage under 35 USC 371

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1		851963_425USPC_Amendment.pdf	551282 7515e7e96bffd464b37dd6753469aa10036 960b0	yes	12

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Amendment/Req. Reconsideration-After Non-Final Reject	1	1
	Claims	2	8
	Applicant Arguments/Remarks Made in an Amendment	9	12

Warnings:

Information:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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